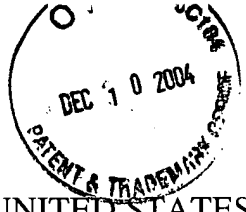


2154
Hw/B



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Where Patent Application of

Confirmation No. 1430

SUZUKI

Allowed: 12/06/2004

Serial No. 09/717,270

Atty Ref.: 2018-354

Filed: November 22, 2000

TC/A.U.: 2154

For: ELECTRONIC CONTROL UNIT HAVING
SINGLE NON-VOLATILE MEMORY FOR
MULTIPLE CENTRAL PROCESSING UNITS
AND DATA RETRIEVAL METHOD

Examiner: F. Moslehi

* * * * *

December 10, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUMMARY OF INTERVIEW

Sir:

The Examiner's Summary of Interview mailed 12/06/2004 includes boiler plate verbage that appears to require the undersigned to file an additional Interview Summary from the applicant's viewpoint within one month. Reference is made to requirements on a reverse side of the attached sheet, however the papers received by the undersigned have a reverse side of the Examiner's Interview Summary Sheet that is entirely blank.

In any event, on November 18, 2004 the undersigned was telephoned by Examiner El Hady to give the undersigned notice that an Examiner's proposed amendment was being sent by facsimile. A copy of that facsimile is attached hereto.

SUZUKI

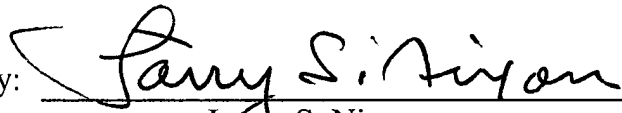
Serial No. 09/717,270

On November 22, 2004, the undersigned telephone Examiner El Hady who was not available so a phone mail message was left explaining that the proposed amendments were not acceptable to the applicant for the most part. The only proposed amendments that could be agreed to were those in claim 6 at line 7, claim 11 at line 5 and claim 13 at line 5.

On November 30, 2004, Examiner El Hady again telephoned the undersigned to simply give notice that, upon further reflection, he had decided to allow the application making only the few agreed upon minor amendments as noted above.

Respectfully submitted,

NIXON & VANDERHYE P.C.

By: 
Larry S. Nixon
Reg. No. 25,640

LSN:vc
1100 North Glebe Road, 8th Floor
Arlington, VA 22201-4714
Telephone: (703) 816-4000
Facsimile: (703) 816-4100



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Fax Cover Sheet

Date: 18 Nov 2004

To: Larry S. Nixon	From: Nabil M El-Hady
Application/Control Number: 09/717,270	Art Unit: 2154
Fax No.: 703-816-4100	Phone No.: (571) 272-3963
Voice No.: 703-816-4000	Return Fax No.: (703) 872-9306
Re: Att. Docket 2018-354	CC:
<input type="checkbox"/> Urgent <input checked="" type="checkbox"/> For Review <input type="checkbox"/> For Comment <input checked="" type="checkbox"/> For Reply <input type="checkbox"/> Per Your Request	

Comments:

A PROPOSED AMEUREMENT TO SPEED UP THE PROSECUTION OF THE ABOVE APPLICATION AND TO PLACE IT IN CONDITION FOR ALLOWANCE.

YOUR PROMPT REPLY IS GREATLY APPRECIATED.

Number of pages 3 including this page

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✓
see

In the claims:

I. In claim 1:

a) line 11, replace "." with -- ;

wherein the single non-volatile memory is connected to each of the plurality of CPUs so that data in the single non-volatile memory is commonly used by the CPUs; and

wherein the first CPU and the second CPU are programmed (a) to retrieve only specified high-priority data from the non-volatile memory in their respective initialization operations, and (b) to retrieve other low-priority data from the non-volatile memory only when required after completion of initialization operations. --

II. Cancel claim 5.

III. In claim 6:

a) line 7, after "command" insert -- to the non-volatile memory --;

b) line 7, replace "the second" with -- a second --;

c) line 9, replace "." with -- ;

wherein the single non-volatile memory is connected to each of the plurality of CPUs so that data in the single non-volatile memory is commonly used by the CPUs; and

wherein the first CPU and the second CPU are programmed (a) to retrieve only specified high-priority data from the non-volatile memory in their respective initialization operations, and (b) to retrieve other low-priority data from the non-volatile memory only when required after completion of initialization operations. --

Art Unit: 2154

IV. In claim 11:

a) line 5, after "command" insert – to the non-volatile memory --;

b) line 9, replace "." with -- ;

wherein the single non-volatile memory is connected to each of the plurality of CPUs so that data in the single non-volatile memory is commonly used by the CPUs; and

wherein the first CPU and the second CPU are programmed (a) to retrieve only specified high-priority data from the non-volatile memory in their respective initialization operations, and (b) to retrieve other low-priority data from the non-volatile memory only when required after completion of initialization operations. --

V. In claim 12:

a) line 4, after "command" insert – from the first CPU --;

VI. In claim 13:

a) line 5, before ", so that" insert – is delayed --.

VII. Cancel claim 15.

VIII. Cancel claim 21.

IX. Cancel claim 22.

X. Cancel claim 23.